	Туре	r ŧ	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	214150	• • •	•	2004/03/27 17:06
2	BRS	L2	55	or semiconductor\$1 or	EPO;	2004/03/27 15:34
3	BRS	L3	13	2 and illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1)same ceramic	:	2004/03/27 16:38
4	BRS	L4	11	3 and angle	:	2004/03/27 16:46
5	BRS	L5	0	4 and (plurality or plural\$3)same pixel near address	:	2004/03/27 16:18
6	BRS	L6	0	4 and (plurality or plural\$3)same pixel near locat\$3		2004/03/27 15:37
7	BRS	L7	4	4 and locat\$3 near3 address	USPAT; EPO; JPO; USOCR	2004/03/27 15:38
8	BRS	L8	8	4 and @ad<20000428	USPAT; EPO; JPO; USOCR	2004/03/27 15:46
9	BRS	L9	0	<pre>2 and illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1)with different near3 angles</pre>	JPO:	2004/03/27 15:46
10	BRS	L10	304	illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1)with different near3 angles	USPAT; EPO; JPO; USOCR	2004/03/27 16:28

	Туре	L #	Hits	Search Text	DBs	Time Stamp
11	BRS	L11	54	10 and("3" adj d or ("3" or three)adj dimensional)		2004/03/27 15:46
12	BRS	L12	46	11 and @ad<20000428	USPAT; EPO; JPO; USOCR	2004/03/27 16:29
13	BRS	L13	4	12 and (plurality or plural\$3)same pixel	:	2004/03/27 16:18
14	BRS	L14	1	•	!	2004/03/27 16:53
15	BRS	L15	249	inspect\$3 near3 surface and mount\$2 near3 components	USPAT; EPO; JPO; USOCR	2004/03/27 16:27
16	BRS	L16	66	15 and("3" adj d or ("3" or three)adj dimensional)	USPAT; EPO; JPO; USOCR	2004/03/27 16:28
17	BRS	L17	0	16 and illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1 or component)with different near3 angles	USPAT; EPO; JPO; USOCR	2004/03/27 16:29
18	BRS	L18	8	16 and illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1 or component)and different near3 angles	USPAT; EPO; JPO; USOCR	2004/03/27 16:29
19	BRS	L19	7	18 and @ad<20000428	USPAT; EPO; JPO; USOCR	2004/03/27 16:46
20	BRS	L20	3	16 and illuminat\$3 and(substrate or wafer\$1 or semiconductor\$1 or chip\$1)same ceramic	USPAT; EPO; JPO; USOCR	2004/03/27 16:47

	Туре	L #	Hits	Search Text	DBs	Time Stamp
21	BRS	L21	3	20 and @ad<20000428		2004/03/27 17:10
22	BRS	L22	56	16 and angle	USPAT; EPO; JPO; USOCR	2004/03/27 16:58
23	BRS	L23	20	22 and threshold	USPAT; EPO; JPO; USOCR	2004/03/27
24	BRS	L25	1	and(substrate or wafer\$1 or semiconductor\$1 or	USPAT; EPO; JPO; USOCR	2004/03/27 16:47
25	BRS	L26	0	<b>}</b>		2004/03/27 17:02
26	BRS	L27	10	24 and pixels and locat\$3	USPAT; EPO; JPO; USOCR	: :
27	BRS	L28	0	27 and substrate near3 address	•	2004/03/27 16:56
28	BRS	L30	1	29 and @ad<20000428	USPAT; EPO; JPO; USOCR	2004/03/27 16:56
29	BRS	L31	1	30 and angle		2004/03/27 17:11
30	BRS	L33	0	i :	USPAT; EPO; JPO; USOCR	2004/03/27 17:01

	Туре	L #	Hits	Search Text	DBs	Time Stamp
31	BRS	L34	0	32 and pixels near3 locat\$3	USPAT; EPO; JPO; USOCR	2004/03/27 16:59
32	BRS	L32	1	31 and threshold		2004/03/27 17:11
33	BRS	L35	0		USPAT; EPO; JPO; USOCR	2004/03/27 17:01
34	BRS	L36	0	29 and pixels	:	2004/03/27 17:01
35	BRS	L37	0	27 and substrate near3 address	USPAT; EPO; JPO; USOCR	2004/03/27
36	BRS	L38	0	27 and locat\$3 near3 address and image\$1		2004/03/27 17:12
37	BRS	L29	2	15 and substrate near3 address	•	2004/03/27 17:05
38	BRS	L24	17	, 23 and @ad<20000428		2004/03/27 17:09
39	BRS	L39	1	24 and substrate near3 address	,	2004/03/27 17:05
40	BRS	L40	1	39 and ("3" adj d or ("3"		2004/03/27 17:08

	Туре	L #	Hits	Search Text	DBs	Time Stamp
41	BRS	L41	0	20 and count\$3	USPAT; EPO; JPO; USOCR	2004/03/27 17:10
42	BRS	L42	2	24 and select\$3 near3 pixel	USPAT; EPO; JPO; USOCR	2004/03/27 17:10
43	BRS	L43	2	42 and count\$3	USPAT; EPO; JPO; USOCR	2004/03/27 17:10
44	BRS	L44	2	43 and @ad<20000428	USPAT; EPO; JPO; USOCR	2004/03/27 17:10
45	BRS	L45	2		USPAT; EPO; JPO; USOCR	2004/03/27
46	BRS	L47	0		USPAT; EPO; JPO; USOCR	2004/03/27 17:12
47	BRS	L48	0	46 and locat\$3 and address near3 image\$1	USPAT; EPO; JPO; USOCR	2004/03/27 17:12
48	BRS	L46	2	45 and angle		2004/03/27 17:12